

CLAIM AMENDMENTS:

Please cancel rejected claims 45-47, and retain the allowed claims as follows:

1. (Previously Presented) A solid-state image pickup apparatus comprising:
 - a photoelectric conversion unit;
 - a transfer unit for transferring signal charges from said photoelectric conversion unit;
 - a capacitance unit for holding the transferred signal charges; and
 - an amplification unit for outputting a signal corresponding to the signal charges held by said capacitance unit,
 - wherein said capacitance unit includes a first capacitance value and an additive capacitance unit for adding a capacitance to said capacitance unit to increase the first capacitance value to obtain a second capacitance value, and
 - wherein a signal read-out from said amplification unit has a first read-out mode in which a signal is read out while holding the signal charges in said capacitance unit and said additive capacitance unit, and a second read-out mode in which a signal is read out while holding the signal charges in said capacitance unit.
2. (Previously Presented) An apparatus according to claim 1, further comprising at least one different additive capacitance unit for further adding a capacitance to said capacitance unit and said additive capacitance unit,

wherein read modes in which a signal is read out from said amplification unit while holding the signal charges in said capacitance unit in correspondence with each of capacitance values obtained by adding said different additive capacitance unit to each of said capacitance unit and said additive capacitance unit are prepared in number corresponding to the number of different additive capacitance units.

3. (Previously Presented) A solid-state image pickup apparatus comprising:

- a photoelectric conversion unit;
- a capacitance unit for holding signal charges transferred from said photoelectric conversion unit;
- a transfer field effect transistor for transferring the signal charges from said photoelectric conversion unit and adding a capacitance formed by a channel to said capacitance unit to increase a capacitance value; and
- an amplification unit for outputting a signal corresponding to the signal charges held by said capacitance unit or the signal charges held by said capacitance unit and the capacitance of said transfer field effect transistor,

wherein a signal read-out from said amplification unit has a first read-out mode in which a signal is read out while holding the signal charges in said capacitance unit and the capacitance of said transfer field effect transistor, and a second read-out mode in which a signal is read out while holding the signal charges in said capacitance unit.

4. (Previously Presented) A solid-state image pickup apparatus comprising:

a photoelectric conversion unit;

a first and a second transfer unit for transferring signal charges from said photoelectric conversion unit;

a first capacitance unit, inserted between said first transfer unit and said second transfer unit for holding the transferred signal charges;

a second capacitance unit, arranged on an output side of said second transfer unit, for holding the transferred signal charges; and

an amplification unit for outputting a signal corresponding to the signal charges held by said first and second capacitance unit or the signal charges held by said second capacitance unit,

wherein a signal read-out from said amplification unit has a first read-out mode in which a signal is read out while holding the signal charges in said first and second capacitance units, and a second read-out mode in which a signal is read out while holding the signal charges in said second capacitance unit.

5. (Previously Presented) An apparatus according to claim 1, further comprising an adding unit for outputting signals in the respective read-out modes.

6. (Previously Presented) An apparatus according to claim 3, further comprising an adding unit for outputting signals in the respective read-out modes.

7. (Previously Presented) An apparatus according to claim 4, further comprising an adding unit for outputting signals in the respective read-out modes.

8. (Previously Presented) A solid-state image pickup apparatus comprising:

- a photoelectric conversion unit;
- a transfer unit for transferring signal charges from said photoelectric conversion unit;
- a capacitance unit for holding the transferred signal charges; and
- an amplification unit for outputting a first signal without holding any signal charges in said capacitance unit and outputting a second signal corresponding to the signal charges while keeping the signal charges held by said capacitance unit,
 - wherein said capacitance unit includes a first capacitance value and an additive capacitance unit for adding a capacitance to said capacitance unit to increase the first capacitance value to obtain a second capacitance value, and
 - wherein a first signal read-out from said amplification unit has a first non-holding read-out mode in which a signal is read out from said capacitance unit without holding any signal charges, and a second non-holding read-out mode in which a signal is read out from said capacitance unit and said additive capacitance unit without holding any signal charges, and
 - a second signal read-out from said amplification unit has a first holding read-out mode in which a signal is read out while keeping the signal charges held

by said capacitance unit, and a second holding read-out mode in which a signal is read out while keeping the signal charges held by said capacitance unit and said additive capacitance unit.

9. (Previously Presented) An apparatus according to claim 8, further comprising at least one different additive capacitance unit for further adding a capacitance to said capacitance unit and said additive capacitance unit,

wherein non-holding read-out modes in which a signal is read out from said amplification unit without holding any signal charges in said capacitance unit in correspondence with each of capacitance values obtained by adding said different additive capacitance unit to each of said capacitance unit and said additive capacitance unit, and holding read-out modes in which a signal is read out from said amplification unit while keeping the signal charges held by said capacitance unit in correspondence with each of capacitance values obtained by adding said different additive capacitance unit to each of said capacitance unit and said additive capacitance unit are prepared in number corresponding to the number of different additive capacitance units.

10. (Previously Presented) A solid-state image pickup apparatus comprising:

a photoelectric conversion unit;
a capacitance unit for holding signal charges transferred from said photoelectric conversion unit;

a transfer field effect transistor for transferring the signal charges from said photoelectric conversion unit and adding a capacitance formed by a channel to said capacitance unit to increase a capacitance value; and

an amplification unit for outputting a first signal without holding any signal charges in said capacitance unit and outputting a second signal corresponding to the signal charges held by said capacitance unit or the signal charges held by said capacitance unit and the capacitance of said transfer field effect transistor,

wherein a first signal read-out from said amplification unit has a non-holding read-out mode in which a signal is read out from said capacitance unit without holding any signal charges, and

a second signal read-out from said amplification unit has a first holding read-out mode in which a signal is read out while keeping the signal charges held by said capacitance unit and the capacitance of said transfer field effect transistor, and a second holding read-out mode in which a signal is read out while keeping the signal charges held by said capacitance unit.

11. (Previously Presented) A solid-state image pickup apparatus comprising:

a photoelectric conversion unit;

a first and a second transfer unit for transferring signal charges from said photoelectric conversion unit;

a first capacitance unit, inserted between said first transfer unit and said second transfer unit, for holding the transferred signal charges;

a second capacitance unit, arranged on an output side of said second transfer unit, for holding the transferred signal charges; and

an amplification unit for outputting a first signal without holding any signal charges in said first and second capacitance units or said second capacitance unit and outputting a second signal corresponding to the signal charges held by said first and second capacitance units or said second capacitance unit,

wherein a first signal read-out from said amplification unit has a first non-holding read-out mode in which a signal is read out from said first and second capacitance units without holding any signal charges, and a second non-holding read-out mode in which a signal is read out from said second capacitance unit without holding any signal charges, and

a second signal read-out from said amplification unit has a first holding read-out mode in which a signal is read out while keeping the signal charges held by said first and second capacitance units, and a second holding read-out mode in which a signal is read out while keeping the signal charges held by said second capacitance unit.

12. (Previously Presented) An apparatus according to claim 8, further comprising a first addition unit for adding output signals in the respective non-holding read-out modes, a second addition unit for adding output signals in the respective holding

read-out modes, and a subtraction unit for subtracting an output from said first addition unit from an output from said second addition unit.

13. (Previously Presented) An apparatus according to claim 10, further comprising a first addition unit for adding output signals in the respective non-holding read-out modes, a second addition unit for adding output signals in the respective holding read-out modes, and a subtraction unit for subtracting an output from said first addition unit from an output from said second addition unit.

14. (Previously Presented) An apparatus according to claim 11, further comprising a first addition unit for adding output signals in the respective non-holding read-out modes, a second addition unit for adding output signals in the respective holding read-out modes, and a subtraction unit for subtracting an output from said first addition unit from an output from said second addition unit.

15. (Previously Presented) An apparatus according to claim 8, further comprising a subtraction unit for subtracting an output signal in the read-out mode from an output signal in the holding read-out mode when the capacitance values are equal or substantially equal in correspondence with the number of capacitance values varying.

16. (Previously Presented) An apparatus according to claim 10, further comprising a subtraction unit for subtracting an output signal in the read-out mode from an

output signal in the holding read-out mode when the capacitance values are equal or substantially equal in correspondence with the number of capacitance values varying.

17. (Previously Presented) An apparatus according to claim 11, further comprising a subtraction unit for subtracting an output signal in the read-out mode from an output signal in the holding read-out mode when the capacitance values are equal or substantially equal in correspondence with the number of capacitance values varying.

18. (Original) An apparatus according to claim 1, wherein said additive capacitance unit or said additive capacitance unit and said different additive capacitance unit comprise variable capacitive elements electrically connected in parallel to said capacitance unit.

19. (Original) An apparatus according to claim 8, wherein said additive capacitance unit or said additive capacitance unit and said different additive capacitance unit comprise variable capacitive elements electrically connected in parallel to said capacitance unit.

20. (Original) An apparatus according to claim 8, wherein the order of the first non-holding read-out mode and the second non-holding read-out mode is the same as that of the first holding read-out mode and the second holding read-out mode.

21. (Original) An apparatus according to claim 11, wherein the order of the first non-holding read-out mode and the second non-holding read-out mode is the same as that of the first holding read-out mode and the second holding read-out mode.

22. (Original) A solid-state image pickup apparatus using, as an area sensor, said solid-state image pickup apparatus of any one of claims 1 to 21.

23. (Original) A solid-state image pickup apparatus using, as a line sensor, said solid-state image pickup apparatus of any one of claims 1 to 21.

24. (Canceled)

25. (Previously Presented), A signal read-out method for a solid-state image pickup apparatus which holds, in a capacitance unit, signal charges generated by a photoelectric conversion unit and outputs a signal corresponding to the signal charges held by the capacitance unit from an amplification unit, said method comprising:

a first read-out mode in which a signal is output from the amplification element while holding the signal charges generated by the photoelectric conversion unit in the capacitance unit set at a first capacitance value; and

a second read-out mode in which, after said first read-out mode, the capacitance value of the capacitance unit is changed from the first capacitance value to a

second capacitance value, and a signal corresponding to the signal charges held by the capacitance unit set at the second capacitance value is output from the capacitance unit, wherein at least one read-out mode in which, after the second read-out mode, the capacitance value of the capacitance unit is changed from the second capacitance value to an arbitrary capacitance value, and a signal corresponding to the signal charges held by the capacitance unit set at the arbitrary capacitance value is output from the amplification unit.

26. (Canceled)

27. (Previously Presented) A signal read-out method for a solid-state image pickup apparatus, comprising:

a holding read-out mode in which a signal corresponding to signal charges generated by a photoelectric conversion unit is output from an amplification unit while keeping the signal charges held by a capacitance unit; and

a non-holding read-out mode in which a signal is output from the amplification unit without holding any signal charges in the capacitance unit,

wherein said holding read-out mode has a first holding read-out mode in which a signal is output from the amplification unit while keeping the signal charges held by the capacitance unit set at a first capacitance value, and a second holding read-out mode in which a signal is output from the amplification unit while keeping the

signal charges held by the capacitance unit whose capacitance value is set at a second capacitance value different from the first capacitance value, and

wherein said non-holding read-out mode has a first non-holding read-out mode in which a signal is output from the amplification unit without holding any signal charges in the capacitance unit set at the first capacitance value, and a second non-holding read-out mode in which a signal is output from the amplification unit without holding any signal charges in the unit whose capacitance value is set at the second capacitance value different from the first capacitance value,

wherein at least one holding read-out mode in which the capacitance value of the capacitance unit is changed from the second capacitance value to an arbitrary capacitance value, and a signal is output from the amplification unit while keeping the signal charges held by the capacitance unit set at the arbitrary capacitance value[[;]], and

wherein at least one non-holding read-out mode in which the capacitance value of the capacitance unit is changed from the second capacitance value to an arbitrary capacitance value, and a signal is output from the amplification unit without holding any signal charges in the capacitance unit set at the arbitrary capacitance value.

28. and 29. (Canceled)

30. (Previously Presented) A solid-state image pickup apparatus including a plurality of pixels, each pixel comprising:

a photoelectric conversion unit;

a holding unit for holding a signal from said photoelectric conversion unit;

a read-out unit for reading out the signal held by said holding unit;

and

a capacitance changing unit for changing a capacitance value of said holding unit; and

a control unit having a first mode in which a signal is read out from said read-out unit while keeping said holding unit set at a first capacitance value by said capacitance changing unit, and a second mode in which a signal is read out from said read-out unit while keeping said holding unit set at a second capacitance value by said capacitance changing unit.

31. (Previously Presented) An apparatus according to claim 30, further comprising an addition unit for adding the signal read out in the first mode and the signal read out in the second mode.

32. (Canceled)

33. (Previously Presented) A solid-state image pickup apparatus comprising:

a photoelectric conversion unit; and

a charge/voltage conversion unit for converting signal charges transferred from said photoelectric conversion unit into a signal voltage,
wherein said charge/voltage conversion unit comprises a plurality of capacitances having different dependences on voltage, and
wherein said charge/voltage conversion unit comprises an impurity diffusion region and a MOS structure unit, and the plurality of capacitances having different dependences on voltage comprise a capacitance formed in the impurity diffusion region and a capacitance formed in said MOS structure unit.

34. (Previously Presented) An apparatus according to claim [[32]] 33, wherein said charge/voltage conversion unit comprises an impurity diffusion region and a buried semiconductor junction portion, and the plurality of capacitances having different dependences on voltage comprise a capacitance formed in the impurity diffusion region and a capacitance formed in said buried semiconductor junction portion.

35. (Previously Presented) An apparatus according to claim 34, wherein the dependence of the capacitance formed in the buried semiconductor junction portion on voltage is controlled by an impurity concentration or depth of a semiconductor region of one conductivity type of the buried semiconductor junction portion.

36. (Previously Presented) An apparatus according to claim 35, wherein the dependence of the capacitance formed in the buried semiconductor junction portion on

voltage is controlled by a width of a semiconductor region of one conductivity type of the buried semiconductor junction portion.

37. (Canceled)

38. (Previously Presented) A solid-state image pickup apparatus comprising:

a photoelectric conversion unit;

a charge/voltage conversion unit for converting signal charges transferred from said photoelectric conversion unit into a signal voltage, wherein said charge/voltage conversion unit comprises a plurality of capacitances having different dependences on voltage;

a reset unit for applying a reset voltage to reset said charge/voltage conversion unit,

wherein a charge/voltage conversion efficiency of said charge/voltage conversion unit is controlled by controlling a voltage value of the reset voltage; and

a sampling unit for sampling a light amount incident on said photoelectric conversion unit, wherein the reset voltage is set in accordance with a sampling signal from said sampling unit.

39. (Original) An apparatus according to claim 38, wherein the sampling signal is a signal one frame ahead.

40. (Original) An apparatus according to claim 38, wherein the sampling signal is one of an overflow drain signal and a smear signal.

41. (Original) An apparatus according to claim 38, wherein before an accumulation period of the signal charges, second signal charges accumulated in an accumulation period shorter than the accumulation period are transferred to the impurity diffusion region, and the second signal charges are used as the sampling signal.

42.-47. (Canceled)